

	<h1>Attollo Engineering</h1>	Document Number:	FRM-020
	<h2>JOB DESCRIPTION</h2>	Document Revision:	001
			Approval Date:

<b>Job Title:</b>	Lead ROIC Design Engineer	<b>Job Category:</b>	Engineering
<b>Department/Group:</b>	Engineering	<b>Job Code:</b>	
<b>Location:</b>	Camarillo, CA / Santa Barbara, CA	<b>Travel Required:</b>	Infrequent
<b>Level/Salary Range:</b>	Competitive pay BOE	<b>Position Type:</b>	Full Time
<b>HR Contact:</b>	jobs@attolloengineering.com	<b>Date posted:</b>	November 25, 2023
<b>Will Train Applicant:</b>	Yes	<b>Posting Expires:</b>	Click here to enter a date.
<b>External posting URL:</b>			

**JOB DESCRIPTION:**

Attollo Engineering is a leader in cutting-edge technology solutions, specializing in advanced photonics, integrated circuit design, and sensor technologies. Our commitment to innovation drives us to develop solutions for some of the most challenging applications in aerospace, defense, and commercial markets. Join a team that thrives on collaboration and technical excellence. Attollo provides competitive salary compensation, a generous insurance package, and 401K matching.

We are seeking a skilled and motivated Readout Integrated Circuit (ROIC) Designer to join our engineering team. As a ROIC Designer at Attollo Engineering, you will play a crucial role in the design, simulation, and implementation of advanced analog and mixed-signal integrated circuits for high-performance imaging and sensing applications.

**ROLE AND RESPONSIBILITIES**

- Design and develop readout integrated circuits (ROIC) for imaging and sensor systems, focusing on analog and mixed-signal circuit design.
- Conduct system-level architecture development and translate system requirements into circuit specifications.
- Perform schematic design, simulation, and layout verification using industry-standard EDA tools.
- Collaborate with cross-functional teams, including systems engineers, photonics experts, and test engineers, to ensure seamless integration and performance of ROICs.
- Participate in prototype testing, validation, and debugging of fabricated ROICs.
- Document design processes, test results, and lessons learned to support future projects.

**QUALIFICATIONS AND EDUCATION REQUIREMENTS**

- Bachelor's, Master's, or PhD in Electrical Engineering or related field.
- 5-10+ years of experience in analog and mixed-signal integrated circuit design, specific expertise in ROICs preferred.
- Proficiency with EDA tools such as Cadence Virtuoso, Spectre, or equivalent.
- Solid understanding of CMOS technology, low-noise design principles, and signal chain analysis.
- Experience with imaging sensors, such as IR or visible imaging technologies, with space environments, and/or with advanced ROIC nodes (90nm, 65nm and smaller) is highly desirable.
- Strong problem-solving skills and the ability to work independently and collaboratively in a fast-paced environment.
- Excellent communication skills for effective collaboration and technical reporting.

**Attollo is an equal opportunity employer, including disability and protected veterans.** All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, disability, or status as a protected veteran.

<b>Reviewed By:</b>	Justin Wehner	<b>Date:</b>	Click here to enter a date.
<b>Approved By:</b>		<b>Date:</b>	Click here to enter a date.
<b>Last Updated By:</b>	Michael MacDougal	<b>Date/Time:</b>	12/5/2024 1:10 PM