

Attollo Engineering

Document Number:	FRM-020
Document Revision:	001
Approval Date:	10/15/2018

JOB DESCRIPTION

Job Title:	Sr. Wafer Process Engineer - Space	Job Category:	Engineering
Department/Group:	Engineering	Job Code:	
Location:	Camarillo, CA / Santa Barbara, CA	Travel Required:	Infrequent
Level/Salary Range:	Competitive pay BOE	Position Type:	Full Time
HR Contact:	jobs@attolloengineering.com	Date posted:	August 26, 2024
Will Train Applicant:	Yes	Posting Expires:	Click here to enter a date.
External posting URL:			

JOB DESCRIPTION:

Attollo Engineering is a manufacturer of ultra-compact and highly functional infrared imaging and LIDAR devices. Attollo is growing fast and is an exciting place to work with great vision for the company and its people. Attollo provides competitive salary compensation, a generous insurance package, and 401K matching.

Attollo is seeking to hire a Senior Process Engineer. In this position you will be heavily involved in developing new manufacturing processes (for R&D and Production) for III-V materials (GaAs, InP, GaSb, etc) for the fabrication of infrared detector arrays, with a particular emphasis on space-based applications. You will be developing and owning new processes as well as implementing existing ones. Use design of experiments to come up with solution paths for open-ended customer-driven challenges. Troubleshoot unforeseen issues, suggest and develop improvements to current approaches. Thoroughly document all process steps and train other engineers in their execution. Collect inspection, metrology and test data and interpret results. Present solutions to internal scientists and management team.

ROLE AND RESPONSIBILITIES

- Develop and optimize processes for making optoelectronic devices
- Develop semiconductor wafers processes around III-V semiconductors (InP, GaAs, GaSb, InAsSb, InGaAs) into optoelectronic devices
- Develop advanced sensor chip assembly processes
- Design process Travelers and Work Instructions with clear instructions for High-Rel missions.
- Track processes and make notes via process travelers
- Run designed experiments to improve process reproducibility and increase yield
- Participate in device test data reviews and suggest process improvements to improve device performance
- Provide engineering and operator oversight

QUALIFICATIONS AND EDUCATION REQUIREMENTS

- Experience with the Operation of nanofabrication equipment including: Photolithographic stepper, metal deposition, dry etching, SEM, and plasma deposition machines
- 5+ years of experience in microelectronic semiconductor fabrication and wafer handling techniques.
- Candidate should possess a proven track record of developing solutions to process development problems.
- Experience with Design of Experiments and Statistical Process Control
- Ability to understand and analyze data related to semiconductor device performance
- Experienced in developing and executing design of experiments.
- Ability to convey data analysis in an accurate and concise format and developing and delivering presentations
- Experience with creating reports, document tests, and creating and reviewing documentation regarding process developed.

PREFERRED SKILLS

• Ability to handle small fragile parts with tweezers



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- Attention to detail
- Experience in mask design and with mask design software
- Program or project management experience
- Ability to mentor team members
- Works well in a fast-paced team environment
- Strong organizational and communication skills
- Excellent writing skills and computer capabilities (MS Office, Python)
- Experience in MES, computer programming and software development
- Strong attention to detail
- Ability to complete tasks with minimal supervision
- Experience with high reliability programs
- Experience with avalanche photodiode detector materials

Attollo is an equal opportunity employer, including disability and protected veterans. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, disability, or status as a protected veteran.

ADDITIONAL NOTES

Reviewed By:		Date:	Click here to enter a date.
Approved By:		Date:	Click here to enter a date.
Last Updated By:	Justin Wehner	Date/Time:	8/26/2024 2:57 PM